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1: Circuits & Layout CMOS VLSI Design
Slide 8 Moore's Law q1965: Gordon
Moore plotted transistor on each chip -
Fit straight line on semilog scale -
Transistor counts have doubled every 26

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Lecture 1: Circuits & Layout

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Basic circuits are then composed to form
macro elements such as multipliers,
where the reader acquires a unified view

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of architectural performance through parallelism, and circuit performance through careful attention to circuit-level and layout design optimization. Topics in analog circuit design reflect the growing tendency for both analog ...

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To realize complex functions of multiple input variables, the basic circuit structures and design principles developed for NOR and NAND can be extended to complex logic gates. The ability to realize complex logic functions, using a small number of transistors is one of the most attractive features of nMOS and CMOS logic circuits.

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Complementary
metal-oxide-semiconductor (CMOS),
also known as complementary-

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symmetry metal-oxide-semiconductor (COS-MOS), is a type of metal-oxide-semiconductor field-effect transistor (MOSFET) fabrication process that uses complementary and symmetrical pairs of p-type and n-type MOSFETs for logic functions. CMOS technology is used for constructing integrated circuit (IC) chips ...

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CMOS VLSI design is broken into two steps: circuit block design and physical design. Circuit block design involves connecting transistors into logic blocks, which are then integrated into a larger

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integrated circuit. Simulation tools are needed to extract the electrical characteristics of your circuit blocks for VLSI.

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